

Main Draw			
GN	[1] Niel ERASMUS		
	A1	[1] Niel ERASMUS	
	BYE		
GC	Enrique DA NAIA		[1] Niel ERASMUS
		6-1, 6-4	
MPUN	JUKA DU PREEZ	Enrique DA NAIA	
		Walkover	
MPUN	[4] Henning ROMIJN		[1] Niel ERASMUS
		6-2, 6-0	
	A3	[4] Henning ROMIJN	
	BYE		
FS	Eduan RADEMAN		[4] Henning ROMIJN
		6-2, 6-4	
	A4	Eduan RADEMAN	
	BYE		
	TAPIWANASHE MUTETWA		[1] Niel ERASMUS
		6-2, 6-4	
	A5	TAPIWANASHE MUTETWA	
	BYE		
	BYE	TAPIWANASHE MUTETWA	
		7-6, 5-7, 10-5	
GN	A6	[3] Wihan SNYDERS	
	[3] Wihan SNYDERS		[2] Ethan DODD
		6-1, 6-0	
LIM	Blake BEKKER		
		6-0, 6-0	
MPUN	Josh FOURIE	Blake BEKKER	
		6-0, 6-0	
	BYE	[2] Ethan DODD	
		6-0, 6-0	
	A8	[2] Ethan DODD	
GN	[2] Ethan DODD		
Position 3-4			
		[4] Henning ROMIJN	
			TAPIWANASHE MUTETWA
			1-6, 6-3, 10-5
Position 5-8			
		Enrique DA NAIA	
			Enrique DA NAIA
			6-0, 6-3
		Eduan RADEMAN	
			[3] Wihan SNYDERS
			6-1, 6-1
		[3] Wihan SNYDERS	
			[3] Wihan SNYDERS
			7-5, 6-2
		Blake BEKKER	
Position 7-8			
		Eduan RADEMAN	
			Blake BEKKER
			6-1, 7-5

BYE
E1
BYE

BYE
E2
BYE

BYE
E3
BYE

Josh FOURIE
E4
BYE

BYE
E5
BYE

BYE
E6
Josh FOURIE

BYE
E7
Josh FOURIE

Josh FOURIE

BYE	
F1	
BYE	

The diagram illustrates a 3-stage pipeline with 3 parallel processors. Each processor has a G1, G2, and G3 stage. The diagram shows the flow of data through the stages, with 'BYE' labels indicating the end of a stage. The output of the G3 stage is connected to a line on the right.

```

graph LR
    subgraph P1 [Processor 1]
        G1_1[G1] --> G2_1[G2]
        G2_1 --> G3_1[G3]
    end
    subgraph P2 [Processor 2]
        G1_2[G1] --> G2_2[G2]
        G2_2 --> G3_2[G3]
    end
    subgraph P3 [Processor 3]
        G1_3[G1] --> G2_3[G2]
        G2_3 --> G3_3[G3]
    end
    G3_1 --> Out
    G3_2 --> Out
    G3_3 --> Out

```

BYE	
H1	
BYE	